

Silicon NPN Power Transistors

2SD1396

DESCRIPTION

- With TO-3PN package
- Built-in damper diode
- High voltage ,high reliability
- High speed switching

APPLICATIONS

- For horizontal output applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector;connected to mounting base
3	Emitter

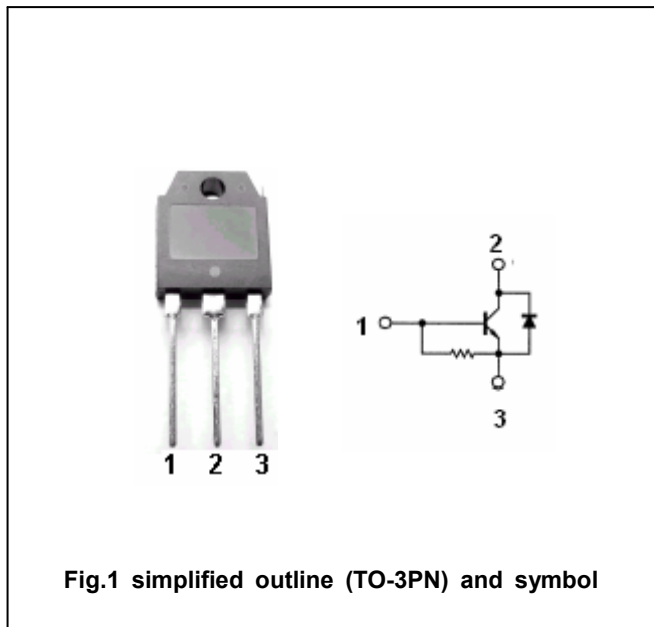


Fig.1 simplified outline (TO-3PN) and symbol

Absolute maximum ratings (Ta=25°C)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	1500	V
V _{CEO}	Collector-emitter voltage	Open base	800	V
V _{EBO}	Emitter-base voltage	Open collector	7	V
I _C	Collector current (DC)		2.5	A
I _{CM}	Collector current-peak		10	A
P _C	Collector power dissipation	T _C =25°C	80	W
T _j	Junction temperature		150	°C
T _{stg}	Storage temperature		-55~150	°C

Silicon NPN Power Transistors

2SD1396

CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector- emitter breakdown voltage	I _C =100mA; R _{BE} =∞	800			V
V _{(BR)CBO}	Collector-base breakdown voltage	I _C =5mA; I _E =0	1500			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =200mA; I _C =0	7			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =2A; I _B =0.6A			8.0	V
V _{BEsat}	Base-emitter saturation voltage	I _C =2A; I _B =0.6A			1.5	V
I _{CBO}	Collector cut-off current	V _{CB} =800V; I _E =0			10	μA
I _{EBO}	Emitter cut-off current	V _{EB} =4V; I _C =0	40		130	mA
h _{FE}	DC current gain	I _C =0.5A ; V _{CE} =5V	8			
f _T	Transition frequency	I _C =0.5A ; V _{CE} =10V		3		MHz
t _f	Fall time	I _C =2A; I _{B1} =0.6A; I _{B2} =-1.2A, V _{CC} =200V; R _L =100Ω			0.7	μs
V _F	Diode forward voltage	I _{EC} =2.5A			2.0	V

